



PATENT
005702-20051

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Tomoharu TANAKA

Serial No: 09/456,340

Filed: December 8, 1999

For: FLASH MEMORY CONTROL METHOD, FLASH
MEMORY SYSTEM USING THE CONTROL
METHOD AND FLASH MEMORY DEVICE
USING THE CONTROL METHOD

TRANSMITTAL OF PRIORITY DOCUMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Art Unit: 2824

Examiner: Not assigned

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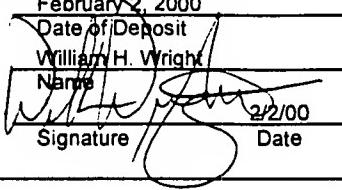
Assistant Commissioner for Patents
Washington D.C. 20231, on

February 2, 2000

Date of Deposit

William H. Wright

Name



Signature

2/2/00

Date

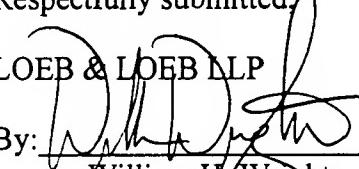
Dear Sir:

Enclosed herewith is a certified copy of Japanese patent application No. 10-351435 which was filed December 10, 1998, from which priority is claimed under 35 U.S.C. § 119 and Rule 55.

Acknowledgment of the priority document(s) is respectfully requested to ensure that the subject information appears on the printed patent.

Respectfully submitted,

LOEB & LOEB LLP

By: 

William H. Wright

Registration No. 36,312

Attorney for Applicant(s)

Date: February 2, 2000

10100 Santa Monica Blvd., 22nd Floor
Los Angeles, California 90067-4164
Telephone: 310-282-2000
Facsimile: 310-282-2192

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別紙添付の書類に記載されている事項は下記の出願書類に記載されて
いる事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed
with this Office.

出願年月日
Date of Application:

1998年12月10日

願番号
Application Number:

平成10年特許第351435号

願人
Applicant(s):

株式会社東芝

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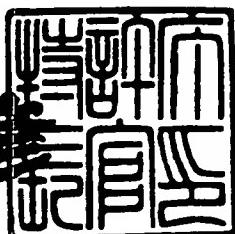
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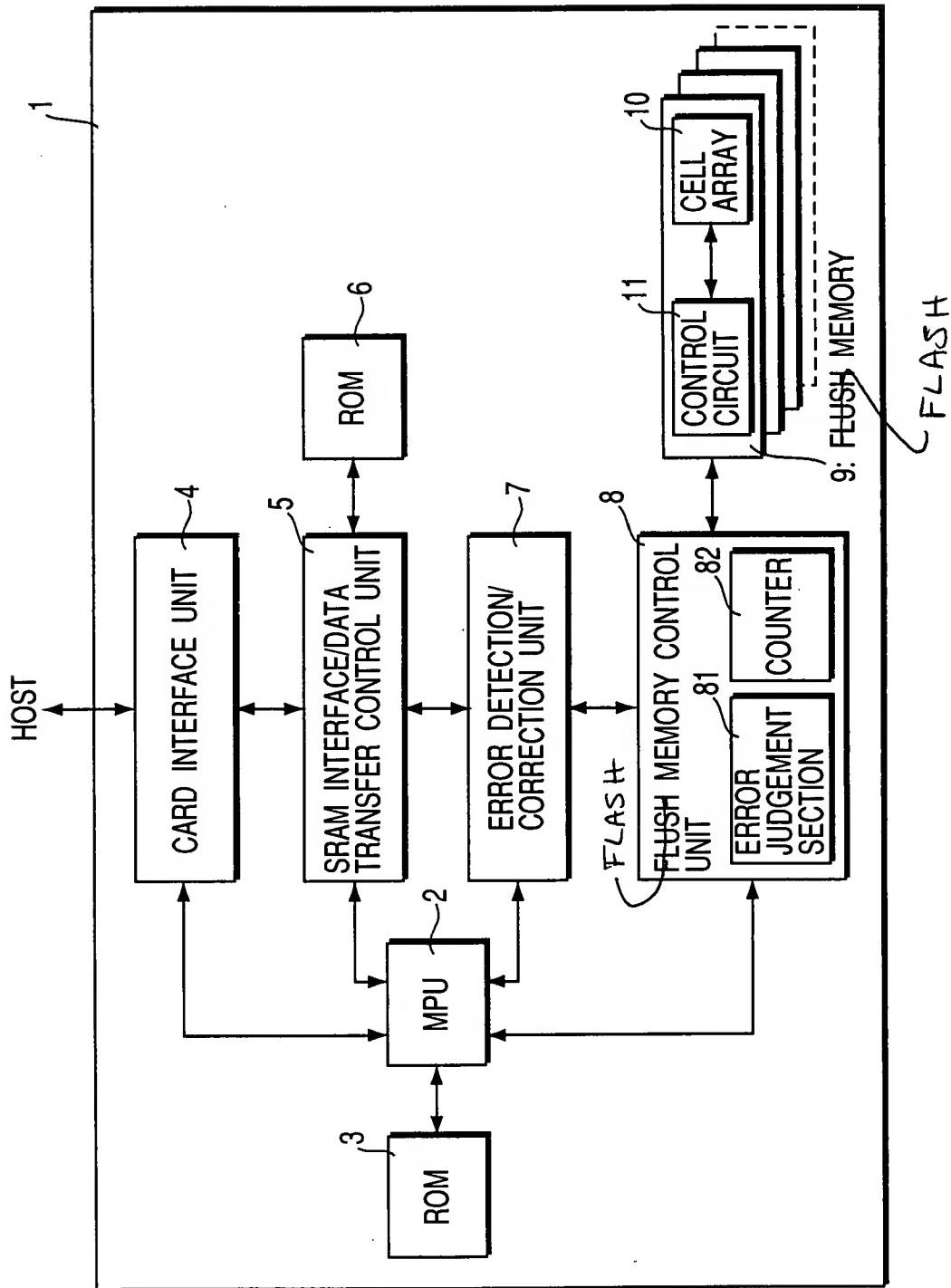
1999年11月26日

特許庁長官
Commissioner,
Patent Office

近藤 隆



出証番号 出証特平11-3083192



Approved
TL 8/16/00

FIG. 1

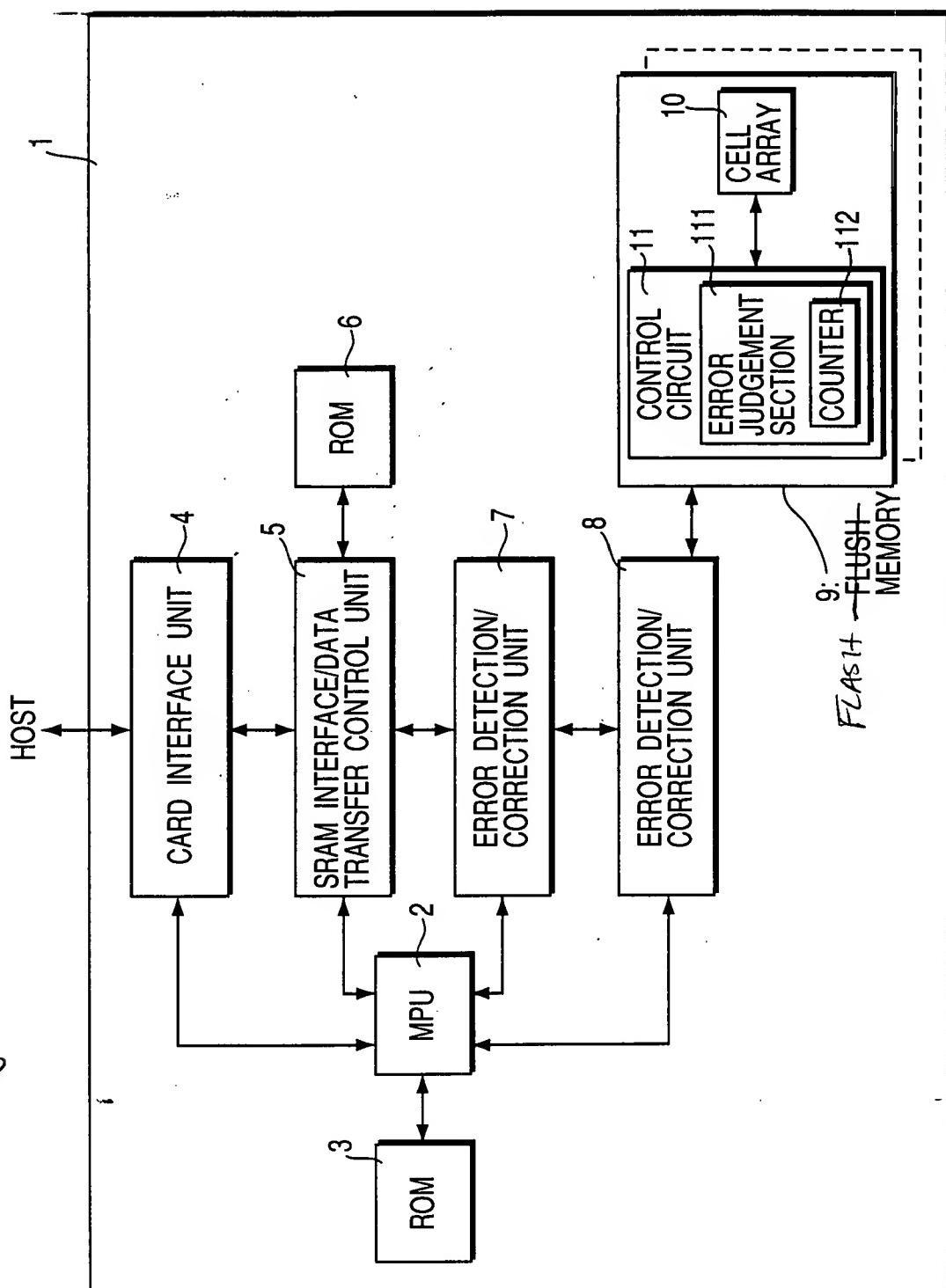


FIG. 6